

CLAIMS

1. (currently amended): A printed circuit board that includes:
a power layer for use in providing electrical power to circuit components;
a ground layer for use in carrying electrical current away from the circuit components; [and]
a loss element residing in an internal layer of the circuit board and connected electrically between the power layer and ground layer to suppress electrical noise caused by changes in current flow in the circuit components; and
a capacitive element connected in series with the loss element.
2. (cancelled)
3. (currently amended): The circuit board of claim 1 [2], where the loss element and the capacitive element reside in two different layers of the circuit board.
4. (cancelled)
5. (currently amended): The circuit board of claim 1 [4], where the loss element resides within an internal power or ground plane.
6. (original): The circuit board of claim 1, where the loss element includes a resistor.
7. (original): The circuit board of claim 6, where the resistor has a resistance value on the order of 1-10 ohms.
8. (original): The circuit board of claim 6, where the resistor is formed from a polymer thick film (PTF) material.
- 9-16. (cancelled)

17. (previously amended): A printed circuit board that includes:
electronic circuitry;
a power layer for use in providing electric current to the electronic circuitry;
and
a loss element residing within the power layer and connected electrically to
the power layer to suppress electrical noise created by sudden changes in current flow in
the electronic circuitry.

18. (cancelled)

19. (original): The circuit board of claim 17, where the loss element includes a
polymer thick film (PTF) resistor.

20. (previously added): A printed circuit board comprising multiple layers,
including:
at least one layer on which electronic circuitry resides;
at least one power layer for use in providing electric current to the electronic
circuitry, comprising:
at least one resistive element formed in a void in the power layer and connected
electrically to the power layer to suppress electrical noise created by sudden changes in
current flow; and
at least one capacitive element connected in series with the resistive element.